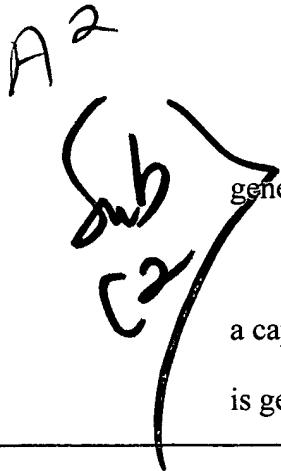


circuit which controls said output voltage from said voltage multiplication circuit, wherein a ratio between a first capacitor of said switched capacitor circuit and a second capacitor determines said rise-time of said signal, said circuit for controlling a rise time of a signal further comprises a constant ramp generator.

14. (Amended) A switched capacitor controller for controlling a rise time of an on-chip generated voltage source, comprising:



a charge pump;
a ramp generator coupled to said charge pump, wherein said ramp generator comprises a switched capacitor;
a regulator circuit coupled to said switched capacitor circuit which causes a capacitor to switch between ground and a node, wherein a stair-step ramp signal is generated and said rise-time is controlled according to said switched capacitor.

REMARKS

Claims 1-3, and 5-21 are pending. The Applicant respectfully requests reconsideration of these Claims based on the amendments made herein and the remarks that follow.

Drawings

The Examiners objection to the Drawings has been obviated by the amendments to the Claims made herein and the remarks that follow. The Applicant respectfully requests the withdrawal of these objections.